



IN THE SPECIFICATION

Please replace the paragraph beginning on page 1, line 2, with the following rewritten paragraph:

U.S. Patent Application ~~Serial No. 09/377, 343~~, now U.S. Patent 6,598,166, entitled "Microprocessor in Which Logic Changes During Execution," filed August 18, 1999 and issued July 22, 2003; U.S. Patent Application ~~Serial No. 09/377,298~~, now U.S. Patent 6,675,298, entitled "Execution of Instructions Using Op Code Lengths Longer Than Standard Op Code Lengths to Encode Data," filed August 18, 1999 and issued January 6, 2004; U.S. Patent Application ~~Serial No. 09/376,655~~, now U.S. Patent 6,609,201, entitled "Secure Program Execution Using Instruction Buffer Interdependencies," filed August 18, 1999 and issued August 19, 2003; U.S. Patent Application ~~Serial No. 09/377,344~~, now U.S. Patent 6,757,831, entitled "Logic Block Used to Check Instruction Buffer Configuration," filed August 18, 1999 and issued June 29, 2004; U.S. Patent Application ~~Serial No. 09/377,299~~, now U.S. Patent 6,665,796, entitled "Microprocessor Instruction Result Obfuscation," filed August 18, 1999 and issued December 16, 2003; and U.S. Patent Application ~~Serial No. 09/377,297~~, now U.S. Patent 6,308,256, entitled "Secure Execution of Program Instructions Provided by Network Interactions with Processor," filed August 18, 1999 and issued October 23, 2001; all invented by Alan C. Folmsbee and commonly assigned, share common subject matter. These applications are incorporated by reference herein in their entirety.

Below is a copy of the above paragraph without markings:

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